

REMARKS

Claim 1 has been further amended and new claims 15-22 have been added.

The previously pending claims were rejected as anticipated by MURAMOTO et al. 6,198,140. Amended claim 1 and new claims 15-22 avoid this rejection and reconsideration and withdrawal of the rejection are respectfully requested.

MURAMOTO et al. discloses a semiconductor device including MOS transistors comprising three types of gate insulating films.

On the other hand, claims 1 and 15-22 relate to a semiconductor integrated circuit device comprising MOS transistors and an MOS type varactor having a gate insulating film which is thinner than the thinnest gate insulating film in the MOS transistors.

In MURAMOTO et al., in order to prevent an electric field applied to the gate insulating film from exceeding a predetermined value, the thickness of the gate insulating film is changed according to the voltage applied to the gate electrode of the MOS transistor. However, in the invention of claim 1, in order to increase the maximum capacitance value ( $C_{max}$ ) of the MOS type varactor, that is, in order to expand the fluctuation range of capacitance, the thickness of the gate insulating film of the MOS type varactor is made thinner than the thinnest gate insulating film in the MOS transistors.

Accordingly, claims 1 and 15-22 avoid the rejection of record and their allowance is respectfully requested.

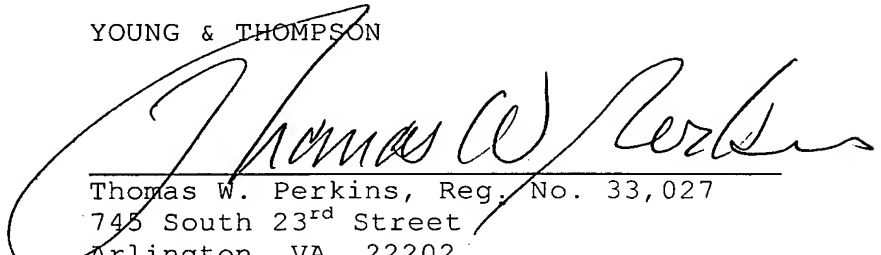
In view of the present further amendment and the foregoing remarks, it is believed that the present application has been placed in condition for allowance. Reconsideration and allowance are respectfully requested.

Please charge the fee of \$100 for the two extra claims of any type added herewith, to Deposit Account No. 25-0120.

The Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 25-0120 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17.

Respectfully submitted,

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